A. Listă 10 lucrări relevante


10. GAITAN, Vasile Gheorghita; GAITAN, Nicoleta Cristina; UNGUREAN, Ioan. CPU Architecture based on a Hardware Scheduler and Independent Pipeline Registers. In: IEEE TRANSACTIONS ON VERY LARGE SCALE INTEGRATION (VLSI) SYSTEMS. 2014. ISSN 1063-8210. DOI:10.1109/TVLSI.2014.2346542, F.I.: 1.245 (Q2), S.R.I.: 1.598 https://doi.org/10.1109/twls.2014.2346542

Ș.L.DR.ING. GĂITAN NICOLETA CRISTINA
Listă de lucrări publicate

B. TEZA DE DOCTORAT


C. BREVET

1. RO133934-A2. Physiological parameter acquisition and processing system has device that receives signals from some acceleration and temperature sensors which are embedded in sensorial element and from other devices. Assignee: GREENSOFT SRL. Inventor(s): GAITAN V G, GAITAN N C, GEMAN O, UNGUREAN I, PETRARIU A I, ZAGAN I, AGHION C, HAGAN M G. Derwent Primary Accession Number: 2020-276046. International Patent Classification: A61B-005/04; A61B-005/0402.

D. CĂRȚI ȘI CAPITOLE ÎN CARTI


E. ARTICOLE IN REVISTE DIN FLUXUL STINTIFIC INTERNATIONAL PRINCIPAL


16. GAITAN, Vasile Gheorghita; GAITAN, Nicoleta Cristina; UNGUREAN, Ioan. CPU Architecture based on a Hardware Scheduler and Independent Pipeline Registers. In: IEEE TRANSACTIONS ON VERY LARGE SCALE INTEGRATION (VLSI) SYSTEMS. 2014. ISSN 1083-8210. DOI:10.1109/TVLSI.2014.2346542, F.I: 1.245 (Q2), S.R.I: 1.598

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